

## SPECIFICATION

Please change the word “ialways” on page 5, line 13 to “always”.

The voltage generator of FIG. 3 including the pull-up/down drive 32 and the MOS resistance/diode reference voltage generator 33, does not operate with two transistors simultaneously turned on because a gate voltage of the driver transistors MN2 and MP2 ~~ialways~~ always changes in the same direction as the VBL node voltage. Thus, the half Vcc voltage generator of can accurately maintain the VBL node at half the VCC level.